



FRONTGRADE

APPLICATION NOTE

UT700

LEON 3FT Clarification for Unused Pins

8/8/2016

Version #: 1.0.0

Table 1: Cross Reference of Application Products

Product Name	Manufacturer Part Number	SMD#	Device Type	Internal PIC1 Number:
LEON 3FT	UT700	5962-13238	ALL	WQ03

PIC=Product Identification Code

1.0 Overview

The purpose of this application note is to make best practices recommendations for how to handle the unused pins on the UT700 LEON 3FT Microprocessor.

2.0 Legend of Pin Types

Pin Function	Description
I	CMOS
IS	CMOS input Schmitt
O	CMOS output
I/O	CMOS bi-directional
OD	CMOS open drain
PCI-I	PCI input
PCI-O	PCI output
PCI-I/O	PCI bi-directional
PCI-3	PCI tri-state

3.0 UT700 Unused Pin Connection Table

These are recommendations only. If users are concerned about noise, stiffer pullup or pulldown values may be used. Please note that the lower the value of resistor, the more current will be sourced or sunk in the event certain pins inadvertently enabled.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
SYCLK	I	Y20	N/A	Main system clock. The part will not function without a System Clock. Do not leave this signal unconnected for any reason.
NODIV	I	E19	N/A	Clock divider input. Set to '1' for 1x memory clock, '0' for 1/2x memory clock, relative to SYCLK.
$\overline{\text{RESET}}$	IS	L19	Pull High using a 10k resistor	System reset. If pulled low, the part is stuck in permanent reset.
$\overline{\text{ERROR}}$	OD	K19	Pull High using a 10k resistor	Processor error mode indicator. This is an open drain output. Pull to its inactive state.
$\overline{\text{WDOG}}$	OD	J19	Pull High using a 10k resistor	Watchdog indicator. This is an open drain output. Pull to its inactive state.
ADDR[0]	O	W5	NC	Leave unconnected if this signal is not used.
ADDR[1]	O	Y5	NC	Leave unconnected if this signal is not used.
ADDR[2]	O	W6	NC	Leave unconnected if this signal is not used.
ADDR[3]	O	AA5	NC	Leave unconnected if this signal is not used.
ADDR[4]	O	Y6	NC	Leave unconnected if this signal is not used.
ADDR[5]	O	AB5	NC	Leave unconnected if this signal is not used.
ADDR[6]	O	W7	NC	Leave unconnected if this signal is not used.
ADDR[7]	O	AA6	NC	Leave unconnected if this signal is not used.
ADDR[8]	O	Y7	NC	Leave unconnected if this signal is not used.
ADDR[9]	O	AA7	NC	Leave unconnected if this signal is not used.
ADDR[10]	O	AB6	NC	Leave unconnected if this signal is not used.
ADDR[11]	O	W8	NC	Leave unconnected if this signal is not used.
ADDR[12]	O	AB7	NC	Leave unconnected if this signal is not used.
ADDR[13]	O	Y8	NC	Leave unconnected if this signal is not used.
ADDR[14]	O	AA8	NC	Leave unconnected if this signal is not used.
ADDR[15]	O	W9	NC	Leave unconnected if this signal is not used.
ADDR[16]	O	AB8	NC	Leave unconnected if this signal is not used.
ADDR[17]	O	Y9	NC	Leave unconnected if this signal is not used.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
ADDR[18]	O	W10	NC	Leave unconnected if this signal is not used.
ADDR[19]	O	AB9	NC	Leave unconnected if this signal is not used.
ADDR[20]	O	Y10	NC	Leave unconnected if this signal is not used.
ADDR[21]	O	AA9	NC	Leave unconnected if this signal is not used.
ADDR[22]	O	W11	NC	Leave unconnected if this signal is not used.
ADDR[23]	O	AA10	NC	Leave unconnected if this signal is not used.
ADDR[24]	O	Y11	NC	Leave unconnected if this signal is not used.
ADDR[25]	O	AB10	NC	Leave unconnected if this signal is not used.
ADDR[26]	O	AB11	NC	Leave unconnected if this signal is not used.
ADDR[27]	O	AA11	NC	Leave unconnected if this signal is not used.
DATA[0]	I/O	W12	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[1]	I/O	W13	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[2]	I/O	Y12	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[3]	I/O	AA13	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[4]	I/O	AA12	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[5]	I/O	AB13	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[6]	I/O	W14	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[7]	I/O	AA14	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[8]	I/O	Y13	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[9]	I/O	W15	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[10]	I/O	AB15	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[11]	I/O	Y14	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[12]	I/O	AB14	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[13]	I/O	W16	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[14]	I/O	AA18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[15]	I/O	Y15	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[16]	I/O	AB16	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[17]	I/O	AA15	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[18]	I/O	AB17	Pull Low or high using a 10k resistor	If unused, pull to a known state.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
DATA[19]	I/O	AA16	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[20]	I/O	AA19	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[21]	I/O	W17	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[22]	I/O	AB18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[23]	I/O	Y16	Pull Low or high using a 10k resistor	If unused, pull to a known state.
DATA[24]	I/O	Y17	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
DATA[25]	I/O	AA17	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
DATA[26]	I/O	W18	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
DATA[27]	I/O	AB19	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
DATA[28]	I/O	Y19	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
DATA[29]	I/O	AB20	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
DATA[30]	I/O	Y18	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
DATA[31]	I/O	AA20	Pull Low or high using a 10k resistor	Note that bits 24 through 31 are the bare minimum to interface with memory in 8 bit mode.
CB[0]	I/O	V19	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[1]	I/O	AA21	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[2]	I/O	Y21	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[3]	I/O	W19	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[4]	I/O	Y22	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[5]	I/O	W20	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[6]	I/O	W22	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[7]	I/O	W21	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[8]	I/O	V18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[9]	I/O	U18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[10]	I/O	T18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[11]	I/O	R18	Pull Low or high using a 10k resistor	If unused, pull to a known state.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
CB[12]	I/O	P18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[13]	I/O	N18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[14]	I/O	M18	Pull Low or high using a 10k resistor	If unused, pull to a known state.
CB[15]	I/O	M19	Pull Low or high using a 10k resistor	If unused, pull to a known state.
$\overline{\text{WRITE}}$	O	V21	NC	If not used leave unconnected
$\overline{\text{OE}}$	O	U19	NC	If not used leave unconnected.
$\overline{\text{IOS}}$	O	T20	NC	If not used leave unconnected.
$\overline{\text{ROMS}}[0]$	O	V22	NC	If not used leave unconnected.
$\overline{\text{ROMS}}[1]$	O	U20	NC	If not used leave unconnected.
$\overline{\text{RWE}}[0]$	O	U22	NC	If not used leave unconnected.
$\overline{\text{RWE}}[1]$	O	T19	NC	If not used leave unconnected.
$\overline{\text{RWE}}[2]$	O	T22	NC	If not used leave unconnected.
$\overline{\text{RWE}}[3]$	O	T21	NC	If not used leave unconnected.
$\overline{\text{RAMOE}}[0]$	O	V20	NC	If not used leave unconnected.
$\overline{\text{RAMOE}}[1]$	O	R21	NC	If not used leave unconnected.
$\overline{\text{RAMOE}}[2]$	O	R20	NC	If not used leave unconnected.
$\overline{\text{RAMOE}}[3]$	O	R22	NC	If not used leave unconnected.
$\overline{\text{RAMOE}}[4]$	O	R19	NC	If not used leave unconnected.
$\overline{\text{RAMS}}[0]$	O	P22	NC	If not used leave unconnected.
$\overline{\text{RAMS}}[1]$	O	P20	NC	If not used leave unconnected.
$\overline{\text{RAMS}}[2]$	O	P21	NC	If not used leave unconnected.
$\overline{\text{RAMS}}[3]$	O	P19	NC	If not used leave unconnected.
$\overline{\text{RAMS}}[4]$	O	N19	NC	If not used leave unconnected.
READ	O	K20	NC	If not used leave unconnected.
$\overline{\text{BEXC}}$	I	K22	Pull High using 10k resistor	Pull to its inactive state.
$\overline{\text{BRDY}}$	I	K21	Pull Low using 10k resistor	Pull to its active state.
SDCLK	O	AB12	NC	If not used leave unconnected.
$\overline{\text{SDRAS}}$	O	N22	NC	If not used leave unconnected.
$\overline{\text{SDCAS}}$	O	N20	NC	If not used leave unconnected.
$\overline{\text{SDWE}}$	O	N21	NC	If not used leave unconnected.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
$\overline{\text{SDCS}}[0]$	O	M21	NC	If not used leave unconnected.
$\overline{\text{SDCS}}[1]$	O	M22	NC	If not used leave unconnected.
SDDQM[0]	O	L21	NC	If not used leave unconnected.
SDDQM[1]	O	M20	NC	If not used leave unconnected.
SDDQM[2]	O	L20	NC	If not used leave unconnected.
CAN_RXD[0]	I	J20	Pull Low using 10k resistor	If not used pull low.
CAN_TXD[0]	O	J22	NC	If not used leave unconnected.
CAN_RXD[1]	I	J21	Pull Low using 10k resistor	If not used pull low.
CAN_TXD[1]	O	H22	NC	If not used leave unconnected.
DSUACT	O	H19	NC	If not used leave unconnected.
DSUBRE	I	H20	Pull Low using 10k resistor	If not used pull low.
DSUEN	I	G19	Pull Low using 10k resistor	If not used pull low.
DSURX	I	G20	Pull Low using 10k resistor	If not used pull low.
DSUTX	O	G21	NC	If not used leave unconnected.
$\overline{\text{TRST}}$	I	F20	Pull Low using 10k resistor	If not used pull low.
TMS	I	F21	Pull Low using 10k resistor	If not used pull low.
TCK	I	G22	Pull Low using 10k resistor	If not used pull low.
TDO	O	F19	NC	If not used leave unconnected.
TDI	I	F22	Pull Low using 10k resistor	If not used pull low.
SPW_CLK	I	A11	Pull Low using 10k resistor	If not used pull low.
SPW_RXS[0]	I	A16	Pull Low using 10k resistor	If not used pull low.
SPW_RXD[0]	I	A15	Pull Low using 10k resistor	If not used pull low.
SPW_TXS[0]	O	B16	NC	If not used leave unconnected.
SPW_TXD[0]	O	B15	NC	If not used leave unconnected.
SPW_RXS[1]	I	A14	Pull Low using 10k resistor	If not used pull low.
SPW_RXD[1]	I	A13	Pull Low using 10k resistor	If not used pull low.
SPW_TXS[1]	O	B14	NC	SpaceWire transmit strobe.
SPW_TXD[1]	O	B13	NC	SpaceWire transmit data.
SPW_RXS[2]	I	A9	Pull Low using 10k resistor	If not used pull low.
SPW_RXD[2]	I	A8	Pull Low using 10k resistor	If not used pull low.
SPW_TXS[2]	O	B9	NC	If not used leave unconnected.
SPW_TXD[2]	O	B8	NC	If not used leave unconnected.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
SPW_RXS[3]	I	A7	Pull Low using 10k resistor	If not used pull low.
SPW_RXD[3]	I	A6	Pull Low using 10k resistor	If not used pull low.
SPW_TXS[3]	O	B7	NC	If not used leave unconnected.
SPW_TXD[3]	O	B6	NC	If not used leave unconnected.
EMDC	O	E22	NC	If not used leave unconnected.
ERX_CLK	I	D22	Pull Low using 10k resistor	If not used pull low.
EMDIO	I/O	D20	Pull High using 10k resistor	If not used pull high.
ERX_COL	I	E21	Pull Low using 10k resistor	If not used pull low.
ERX_CRS	I	E20	Pull Low using 10k resistor	If not used pull low.
ERX_DV	I	D21	Pull Low using 10k resistor	If not used pull low.
ERX_ER	I	C21	Pull Low using 10k resistor	If not used pull low.
ERXD[0]	I	C22	Pull Low using 10k resistor	If not used pull low.
ERXD[1]	I	B21	Pull Low using 10k resistor	If not used pull low.
ERXD[2]	I	C20	Pull Low using 10k resistor	If not used pull low.
ERXD[3]	I	B20	Pull Low using 10k resistor	If not used pull low.
ETXD[0]	O	C19	NC	If not used leave unconnected.
ETXD[1]	O	C18	NC	If not used leave unconnected.
ETXD[2]	O	B18	NC	If not used leave unconnected.
ETXD[3]	O	B19	NC	If not used leave unconnected.
ETX_CLK	I	A19	Pull Low using 10k resistor	If not used pull low.
ETX_EN	O	A18	NC	If not used leave unconnected.
ETX_ER	O	A20	NC	If not used leave unconnected.
EDCLDIS	I	E17	Pull High using 10k resistor	If not used pull low.
$\overline{\text{EMDINT}}$	I	E18	Pull High using 10k resistor	If not used pull high.
GPIO[0]	I/O	B17	Boot Strap. Pull high or low using 10k resistor	Bit 0 Sets memory width, do not leave floating.
GPIO[1]	I/O	C17	Boot Strap. Pull high or low using 10k resistor	Bit 1 sets memory width, do not leave floating.
GPIO[2]	I/O	A17	Boot Strap. Pull high or low using 10k resistor	Bit 2 sets EDAC, do not leave floating.
GPIO[3]	I/O	D17	Pull high or low using 10k	If not used pull high or low.
GPIO[4]	I/O	C16	Pull high or low using 10k	If not used pull high or low.
GPIO[5]	I/O	D16	Pull high or low using 10k resistor	If not used pull high or low.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
GPIO[6]	I/O	C15	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[7]	I/O	D15	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[8]	I/O	C7	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[9]	I/O	B5	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[10]	I/O	D7	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[11]	I/O	A5	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[12]	I/O	D6	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[13]	I/O	C5	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[14]	I/O	C6	Pull high or low using 10k resistor	If not used pull high or low.
GPIO[15]	I/O	D5	Pull high or low using 10k resistor	If not used pull high or low.
RXD	I	C12	Pull high or low using 10k resistor	If not used pull high or low.
TXD	O	C11	NC	If not used leave unconnected.
PCI_AD[0]	PCI-I/O	AA2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[1]	PCI-I/O	AA3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[2]	PCI-I/O	Y1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[3]	PCI-I/O	Y2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[4]	PCI-I/O	Y3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[5]	PCI-I/O	W1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[6]	PCI-I/O	W2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[7]	PCI-I/O	W3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[8]	PCI-I/O	V2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[9]	PCI-I/O	V3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[10]	PCI-I/O	U1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[11]	PCI-I/O	U2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[12]	PCI-I/O	U3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[13]	PCI-I/O	T1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[14]	PCI-I/O	R2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[15]	PCI-I/O	R1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[16]	PCI-I/O	J1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
PCI_AD[17]	PCI-I/O	K2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[18]	PCI-I/O	K1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[19]	PCI-I/O	G1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[20]	PCI-I/O	H3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[21]	PCI-I/O	H2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[22]	PCI-I/O	F1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[23]	PCI-I/O	F2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[24]	PCI-I/O	E1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[25]	PCI-I/O	E2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[26]	PCI-I/O	F3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[27]	PCI-I/O	D1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[28]	PCI-I/O	D2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[29]	PCI-I/O	E3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[30]	PCI-I/O	D3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_AD[31]	PCI-I/O	C1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_RST	PCI-I	C3	Pull low using 2.4k to 8.2k Resistor	If PCI bus is not being used pull low.
PCI_CLK	PCI-I	C2	Clock £ 33.33 MHz	PCI clock input
PCI_C/ \overline{BE} [0]	PCI-I/O	V1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_C/ \overline{BE} [1]	PCI-I/O	P2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_C/ \overline{BE} [2]	PCI-I/O	H1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_C/ \overline{BE} [3]	PCI-I/O	G2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
PCI_PAR	PCI-I/O	P1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_FRAME}$	PCI-3	L1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_IRDY}$	PCI-3	L2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_TRDY}$	PCI-3	M1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_STOP}$	PCI-3	N1	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_DEVSEL}$	PCI-3	M2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_PERR}$	PCI-3	N2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_IDSEL}$	PCI-I	G3	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_REQ}$	PCI-O	A4	NC	If not used leave unconnected.
$\overline{PCI_GNT}$	PCI-I	B2	Pull High using 2.4k to 8.2k Resistor	If not used pull high.
$\overline{PCI_HOST}$	PCI-I	AB3	Pull High using 2.4k to 8.2k resistor	If not used pull high.
$\overline{PCI_ARB_REQ}[0]$	PCI-I	B4	Pull High using 2.4k to 8.2k resistor	If not used pull high.
$\overline{PCI_ARB_REQ}[1]$	PCI-I	AB4	Pull High using 2.4k to 8.2k resistor	If not used pull high.

Pin Name	Direction	Pin Number 484 CLGA	Connection Unused	Comments
PCI_ARB_REQ[2]	PCI-I	Y4	Pull High using 2.4k to 8.2k resistor	If not used pull high.
PCI_ARB_REQ[3]	PCI-I	T3	Pull High using 2.4k to 8.2k resistor	If not used pull high.
PCI_ARB_REQ[4]	PCI-I	P3	Pull High using 2.4k to 8.2k resistor	If not used pull high.
PCI_ARB_REQ[5]	PCI-I	M3	Pull High using 2.4k to 8.2k resistor	If not used pull high.
PCI_ARB_REQ[6]	PCI-I	K3	Pull High using 2.4k to 8.2k resistor	If not used pull high.
PCI_ARB_REQ[7]	PCI-I	C4	Pull High using 2.4k to 8.2k resistor	If not used pull high.
PCI_ARB_GNT[0]	PCI-O	B3	NC	If not used leave unconnected.
PCI_ARB_GNT[1]	PCI-O	AA4	NC	If not used leave unconnected.
PCI_ARB_GNT[2]	PCI-O	W4	NC	If not used leave unconnected.
PCI_ARB_GNT[3]	PCI-O	R3	NC	If not used leave unconnected.
PCI_ARB_GNT[4]	PCI-O	N3	NC	If not used leave unconnected.
PCI_ARB_GNT[5]	PCI-O	L3	NC	If not used leave unconnected.
PCI_ARB_GNT[6]	PCI-O	J3	NC	If not used leave unconnected.
PCI_ARB_GNT[7]	PCI-O	A3	NC	If not used leave unconnected.
1553CLK	I	B11	Pull low using 10k resistor	If not used pull low.
1553RXA	I	C13	Pull low using 10k resistor	If not used pull low.
$\overline{1553RXA}$	I	D12	Pull low using 10k resistor	If not used pull low.
1553RXB	I	C8	Pull low using 10k resistor	If not used pull low.
$\overline{1553RXB}$	I	C9	Pull low using 10k resistor	If not used pull low.
1553RXENA	O	D11	NC	If not used leave unconnected.
1553RXENB	O	D9	NC	If not used leave unconnected.
1553TXINHA	O	D13	NC	If not used leave unconnected.
1553TXINHB	O	D10	NC	If not used leave unconnected.
1553TXA	O	D14	NC	If not used leave unconnected.
$\overline{1553TXA}$	O	C14	NC	If not used leave unconnected.
1553TXB	O	B10	NC	If not used leave unconnected.
$\overline{1553TXB}$	O	C10	NC	If not used leave unconnected.

Revision History

Date	Revision #	Author	Change Description	Page #
08/8/2016	1.0.0	N/A	Initial release	

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