AEROSPACE DATA EXCHANGE PROGRAM TRANSMITTAL



PROBLEM ADVISORY

1. TITLE			2. DOCUMENT NUMBER				
Microcircuit, Digital, CMOS, Radiation Hardened, Monolithic			SPO-2020-PA-0004				
Silicon LEON3/FT AHB Lock Release during Atomic Operation							
			3. DATE (Year, Month, Date)				
			2018, February 22nd				
4. MANUFACTURER NAME AND ADDRESS CAES 4350 CENTENNIAL BOULEVARD COLORADO SPRINGS, COLORADO 80907-3486			5. MANUFACTURER POINT OF CONTACT NAME				
			Gaisler Support				
			6. MANUFACTURER POINT OF CONTACT TELEPHONE				
			+46 (0) 31 775 8650				
			7. MANUFACTURER POINT OF CONTACT EMAIL				
			support@gaisler.com				
8. CAGE CODE	9. LDC START	10. LDC END	11. PRODUCT IDENTIFICATION CODE	12. BASE PART			
65342	1507/1535	1906/1939	See table below for PA	See table below			
13. BLANK			14. SMD NUMBER	15. DEVICE TYPE DESIGNATOR			
			See table below	01			
			16. RHA LEVELS	17. QML LEVEL			
			R	See table below			
			18. NON QML LEVEL	19. GIDEP NUMBER			
			N/A	GB4-P-20-04A			
20. PROBLEM DESCRIPTION / DISCUSSION / EFFECT							

This document describes a corner case that affects specific versions of the LEON3/FT core that can cause the LEON3/FT core to lose the bus lock before making the store operation of an atomic instruction. The corner case can only be triggered when several additional conditions, including the use of the processor's memory management unit, are satisfied during execution.

The issue is present in all LEON3/FT cores implemented with memory management unit (MMU) up to GRLIB build b4204.

Base part number	PIC	SMD number	QML Level
UT699	WQ07	5962-108228	Ø
UT699E	WQ02	5962-13237	Q
UT700	WQ03	5962-13238	Q,V

21. ACTION TAKEN / PLANNED

LEON3/FT AHB Lock Release during Atomic Operation Technical Note 2017-12-21

Doc. No GRLIB-TN-0011

Issue 1.4

22. DISPOSITIONARY RECOMM 23. ENDATION:	CHECK & X	CONTACT MANUFACTURER	REMOVE & REPLACE	CORRECT & USE AS SPECIFIED
24. ADEPT REPRESENTATIVE	25. SIGNATURE			26. DATE
Lin-Chi Huang	Dish / Jung		2018, February 22nd	