


AEROSPACE DATA EXCHANGE PROGRAM TRANSMITTAL

PROBLEM ADVISORY



1. TITLE FTMCTRL: Failing SDRAM Access After Uncorrectable EDAC Error			2. DOCUMENT NUMBER SPO-2020-PA-0001																
			3. DATE (Year, Month, Date) 2018, February 22nd																
4. MANUFACTURER NAME AND ADDRESS CAES 4350 CENTENNIAL BOULEVARD COLORADO SPRINGS, COLORADO 80907-3486			5. MANUFACTURER POINT OF CONTACT NAME Gaisler Support																
			6. MANUFACTURER POINT OF CONTACT TELEPHONE +46 (0) 31 775 8650																
			7. MANUFACTURER POINT OF CONTACT EMAIL support@gaisler.com																
8. CAGE CODE 65342	9. LDC START 1836/1507/1535	10. LDC END 1935/1906/1939	11. PRODUCT IDENTIFICATION CODE See table below for PA	12. BASE PART See table below															
13. BLANK			14. SMD NUMBER See table below	15. DEVICE TYPE DESIGNATOR 01															
			16. RHA LEVELS R	17. QML LEVEL See table below															
			18. NON QML LEVEL N/A	19. GIDEP NUMBER GB4-P-20-01A															
			20. PROBLEM DESCRIPTION / DISCUSSION / EFFECT This document describes a bug present in the SDRAM controller of CAES Gaisler's Fault-Tolerant Memory Controller (FTMCTRL) IP core where an AMBA access following an access that resulted in uncorrectable EDAC error may return erroneous data or an AMBA ERROR response. The erratum is present in the SDRAM controller of FTMCTRL. FTMCTRL implementations that are implemented without SDRAM support and devices that do not make use of SDRAM are not affected.																
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Base part number</th> <th>PIC</th> <th>SMD number</th> <th>QML Level</th> </tr> </thead> <tbody> <tr> <td>UT699</td> <td>WG07</td> <td>5962-08228</td> <td>Q,V</td> </tr> <tr> <td>UT699E</td> <td>WQ02</td> <td>5962-13237</td> <td>Q</td> </tr> <tr> <td>UT700</td> <td>WQ03</td> <td>5962-13238</td> <td>Q,V</td> </tr> </tbody> </table>				Base part number	PIC	SMD number	QML Level	UT699	WG07	5962-08228	Q,V	UT699E	WQ02	5962-13237	Q	UT700	WQ03	5962-13238	Q,V
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21. ACTION TAKEN / PLANNED FTMCTRL: Failing SDRAM Access After Uncorrectable EDAC Error Technical note 2015-10-27 Doc. No GRLIB-TN-0007 Issue 1.0																			
22. DISPOSITIONARY RECOMM 23. ENDATION:		CHECK & <input checked="" type="checkbox"/> USE AS IS	CONTACT <input type="checkbox"/> MANUFACTURER	REMOVE & <input type="checkbox"/> REPLACE	CORRECT & <input type="checkbox"/> USE AS SPECIFIED														
24. ADEPT REPRESENTATIVE Lin-Chi Huang		25. SIGNATURE 		26. DATE 2018, February 22nd															