Dual 1.5A Adjustable Positive/Negative Voltage Regulators

## VRG8601/02

#### **Features**

• Manufactured using Linear Technology Corporation ® Space Qualified RH117 and RH137 die

· Radiation performance

- Total dose: 100 krad(Si),

Dose rate = 50-300 rad(Si)/s

- ELDRS: 50 krad(Si),

Dose rate  $\leq$  10 mrad(Si)/s

- Two-Independent voltage regulators
- Thermal shutdown
- 1.5A output current
- Adjustable Output Voltages
- Packaging Hermetic metal
  - Thru-hole or Surface mount
  - 6 Leads, .655"L x .415"W x .200"Ht
  - Power package
  - Weight 5 gm max
- Designed for aerospace and high reliability space applications
- Positive regulator features (RH117)
  - Output voltage adjustable: +1.25V to 37V
  - 3-Terminal

Voltage reference: 1.25VLoad regulation: 1.9% maxLine regulation: 0.06% maxRipple rejection: >66dB

• Negative regulator features (RH137)

- Output voltage adjustable: -1.25V to -27V

- 3-Terminal

Voltage reference: 1.25V
Load regulation: 1.0% typ
Line regulation: .05% typ
Ripple rejection: >66dB

• Radiation Hardness Assurance Plan: DLA Certified to MIL-PRF-38534, Appendix G.

#### **Description**

The VGR8601/02 consists of a positive (RH117) and a negative (RH137) voltage regulator each capable of supplying in excess of 1.5Amps over the output voltage range as defined under recommended operating conditions. Each regulator is exceptionally easy to set-up, requiring only 2 external resistors to set the output voltage. The module design has been optimized for excellent regulation and low thermal transients. There is full electrical isolation between positive and negative regulators and each regulator to the package.

Further, the VRG8601/02 features internal current limiting, thermal shutdown and safe-area compensation, making them virtually blowout-proof against overloads. The VRG8601/02 serves a wide variety of applications including local on-card regulation, programmable output voltage regulation or precision current regulation.

The VRG8601/02 has been specifically designed to meet exposure to radiation environments. The VRG8601 is configured for a Thru-Hole 6 lead metal power package and the VRG8602 is configured for a Surface Mount 6 lead metal power package. It is guaranteed operational from -55°C to +125°C. Available screened to MIL-STD-883, the VRG8601/02 is ideal for demanding military and space applications.

For detailed performance characteristic curves, applications information and typical applications see the latest Linear Technology Corporation ® data sheets for their RH/LT117 and RH/LT137.



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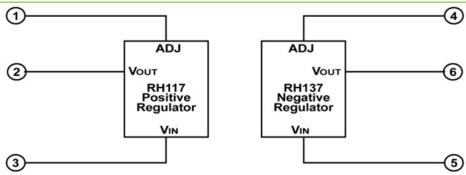


Figure 1 – Block Diagram / Schematic

### **Absolute Maximum Ratings**

Parameter	Range	Units
Operating (Junction) Temperature Range	-55 to +150	°C
Lead Temperature (soldering, 10 sec)	300	°C
Storage Temperature Range	-65 to +150	°C
Input-Output Voltage Differential	40 (Pos) 30 (Neg)	V
Thermal Resistance (junction to case $\Theta_{JC}$ ) each, Pos. & Neg.	5	°C/W
ESD Rating	1.999 <sup>1</sup> /	KV

1/ Meets ESD testing per MIL-STD-883, method 3015, Class 1C.

**Notice:** Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may affect device reliability..

#### **Recommended Operating Conditions**

Parameter	Range	Units
Output Voltage Range		
Positive Voltage Regulator	1.3 to 37	$V_{DC}$
Negative Voltage Regulator	-1.3 to -27	$V_{DC}$
Case Operating Temperature Range	-55 to +125	°C



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### Positive Regulator – Electrical Performance Characteristics $\underline{\mathbf{1}}/$

Parameter	SYM	Conditions ( $P \le P_{MAX}$ )	MIN	MAX	Units	
Reference Voltage <u>4</u> /	$V_{REF}$	$3V \le (V_{IN} - V_{OUT}) \le V_{DIFF MAX}, 10mA \le I_{OUT} \le I_{MAX}$	1.20	1.30	V	
Line Regulation 2/ 4/	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	$3V \le (V_{IN} - V_{OUT}) \le V_{DIFF MAX}, I_{OUT} = 10 \text{mA}$	-	0.03	%/V	
Load Regulation 2/ 4/	$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	$\begin{array}{l} 10 \text{mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}\text{, } V_{\text{OUT}} \; \leq 5 \text{V} \\ 10 \text{mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}\text{, } V_{\text{OUT}} \geq 5 \text{V} \end{array}$	-	60 1.2	mV %	
Thermal Regulation		$I_{OUT} = 1.5A$ , $(V_{IN} - V_{OUT}) = 13.3V$ , 20ms Pulse, 20W, $T_C = +25^{\circ}C$	-	0.07	%/W	
Ripple Rejection Ratio		$V_{OUT} = 10V$ , $f = 120Hz$ , $C_{ADJ} = 10\mu F$	66	-	dB	
Adjustment Pin Current 4/	I <sub>ADJ</sub>	$I_{OUT} = 10 \text{mA}, 3V \le (V_{IN} - V_{OUT}) \le 40V$	-	100	μΑ	
Adjustment Pin Current Change 4/	$\Delta I_{ADJ}$	$10\text{mA} \le I_{\text{OUT}} \le I_{\text{MAX}}$ $3\text{V} \le (\text{V}_{\text{IN}} - \text{V}_{\text{OUT}}) \le 40\text{V}$	-	5	μА	
Minimum Load Current 3/ 4/	I <sub>MIN</sub>	$(V_{IN} - V_{OUT}) = 40V$	-	5	mA	
Current Limit 4/ E/	т	$(V_{IN} - V_{OUT}) \le 15V$	1.5	-	^	
Current Limit 4/5/ I <sub>MAX</sub>		$(V_{IN} - V_{OUT}) = 40V, T_C = +25^{\circ}C$	0.30	-	- A	
Long Term Stability 3/	$\frac{\Delta V_{OUT}}{\Delta T_{IME}}$	T <sub>C</sub> = +125°C		1	%	
Thermal Resistance, each Regulator (Junction to Case) 3/	Θις		-	5	°C/W	



Dual 1.5A Adjustable Positive/Negative Voltage Regulators

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#### Negative Regulator – Electrical Performance Characteristics 1/

Parameter	SYM	Conditions	MIN	MAX	Units
Reference Voltage 4/	$V_{REF}$	$3V \le (V_{IN} - V_{OUT}) \le V_{DIFF MAX}, 10mA \le I_{OUT} \le I_{MAX}$	-1.200	-1.300	V
Line Regulation <u>2</u> / <u>4</u> /	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	$3V \le (V_{IN} - V_{OUT}) \le 30V,$	-	0.05	%/V
		$10\text{mA} \le I_{\text{OUT}} \le I_{\text{MAX}}, V_{\text{OUT}} \le 5V$	-	25	mV
Load Regulation <u>2</u> / <u>4</u> /	$rac{\Delta V_{OUT}}{\Delta I_{OUT}}$	$10\text{mA} \le I_{\text{OUT}} \le I_{\text{MAX}}, V_{\text{OUT}} \ge 5V$	-	1.0	%
Thermal Regulation		$I_{OUT} = 1.5A$ , $(V_{IN} - V_{OUT}) = 13.3V$ , 20ms Pulse, 20W, $T_C = +25^{\circ}C$	-	0.02	%/W
Ripple Rejection		$V_{OUT} = -10V$ , $f = 120Hz$ , $C_{ADJ} = 10\mu F$	66	-	dB
Adjustment Pin Current 4/	$I_{ADJ}$	$I_{OUT} = 10 \text{mA}, 3V \le (V_{IN} - V_{OUT}) \le 30V$	-	100	μΑ
Adjustment Pin Current Change 4/	$\Delta I_{ADJ}$	$10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$ $3\text{V} \leq (\text{V}_{\text{IN}} - \text{V}_{\text{OUT}}) \leq 30\text{V}$	-	5	μΑ
Minimum Lond Cumont 2/4/	т	$(V_{IN} - V_{OUT}) = 30V$	-	5	A
Minimum Load Current <u>3</u> / <u>4</u> /	${ m I}_{\sf MIN}$	$(V_{IN} - V_{OUT}) \le 10V$	-	3	mA
Commont Limit 4/ F/	т	$(V_{IN} - V_{OUT}) \le 15V$	1.5	-	۸
Current Limit <u>4</u> / <u>5</u> /	${ m I}_{\sf MAX}$	$(V_{IN} - V_{OUT}) = 40V, T_C = +25^{\circ}C$	0.24	-	Α
Long Term Stability 3/	$\frac{\Delta V_{OUT}}{\Delta T_{IME}}$	T <sub>C</sub> = +125°C	-	1	%
Thermal Resistance, each Regulator (Junction to Case) 3/	Θις		-	5	°C/W

#### Notes:

- 1) Unless otherwise specified, these specifications apply for (Vin Vout) = 5V, Iout = 0.5A and -55°C < Tc < +125°C.
- 2) Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation. Measurements taken at the output lead must be adjusted for lead resistance.
- 3) Not tested. Shall be guaranteed to the specified limits.
- 4) Specification derated to reflect High Dose Rate (1019 condition A) to 100 krad(Si) and Low Dose Rate (1019 condition D) to 50 krad(Si), @ +25°C.
- 5) Pulsed @ < 10% duty cycle.



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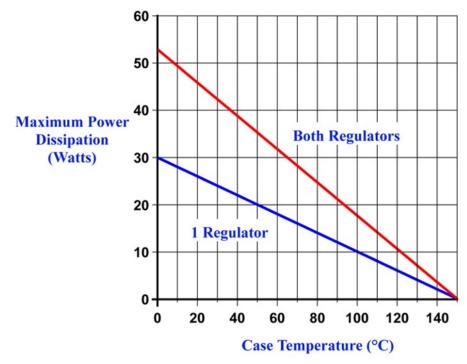


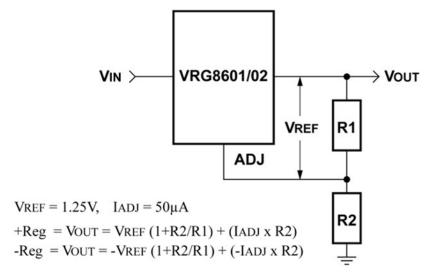
Figure 2 – Maximum Power vs CASE Temperature

The maximum Power dissipation is limited by the thermal shutdown function of each regulator chip in the VRG8601/02. The graph above represents the achievable power before the chip shuts down. The first line in the graph represents the maximum power dissipation of the VRG8601/02 with one regulator on (the other off) and the other line represents both regulators on dissipating equal power. If both regulators are on and one regulator is dissipating more power that the other, the maximum power dissipation of the VRG8601/02 will fall between the two lines. This graph is based on the maximum junction temperature of 150°C and a thermal resistance ( $\Theta_{JC}$ ) of 5°C/W.



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Adjustable Regulator Figure 3 – Typical Applications

#### **Pin Numbers vs Function**

Pin	Function
1	POS_ADJ_1
2	POS_V <sub>OUT</sub> _1
3	POS_V <sub>IN</sub> _1
4	NEG_ADJ_2
5	NEG_V <sub>IN</sub> _2
6	NEG_V <sub>OUT</sub> _2



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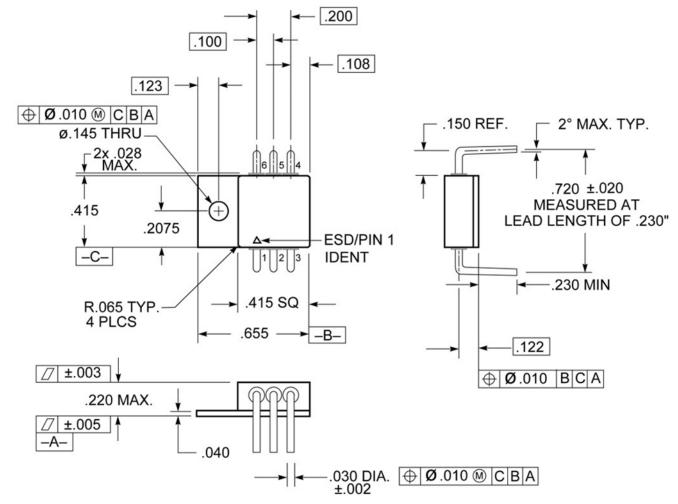


Figure 4 – Package Outline — Thru-Hole Power Package

#### Notes:

- 1) Dimension Tolerance: ±.005 inches.
- 2) Package contains BeO substrate
- 3) Case electrically isolated

#### **Pin Numbers vs Function**

Pin	Function
1	POS_ADJ_1
2	POS_V <sub>OUT</sub> _1
3	POS_V <sub>IN</sub> _1
4	NEG_ADJ_2
5	NEG_V <sub>IN_</sub> 2
6	NEG_V <sub>OUT</sub> _2



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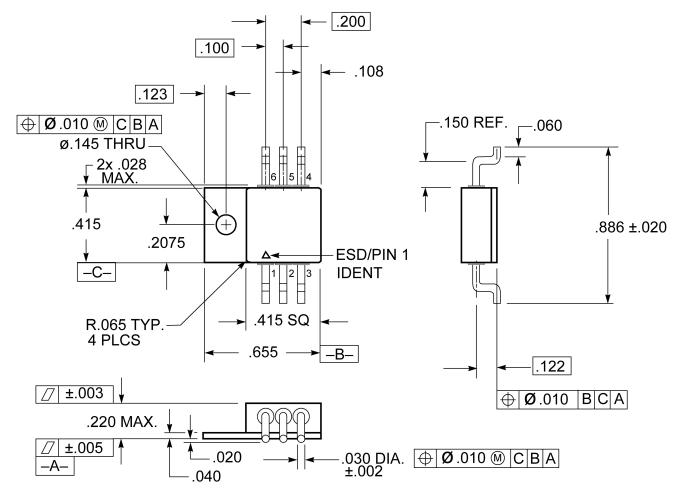


Figure 5 – Package Outline — Surface Mount Power Package

#### **Notes:**

- 1) Dimension Tolerance: ±.005 inches.
- 2) Package contains BeO substrate
- 3) Case electrically isolated

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### **Ordering Information**

Model	DLA SMD #	Screening	Package
VRG8601-7	-	Commercial Flow, +25°C testing only	
VRG8601-S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	6 Lead
VRG8601-201-1S	5962-0521901KXC	In accordance with DLA SMD	Thru-Hole Power Pkg
VRG8601-201-2S	5962-0521901KXA	In accordance with DLA SIND	rowei rkg
VRG8601-901-1S	5962R0521901KXC	In accordance with DLA Certified RHA Program Plan to	
VRG8601-901-2S	5962R0521901KXA	RHA level "R", 100 krad(Si)	
VRG8602-7	-	Commercial Flow, +25°C testing only	
VRG8602-S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	6 Lead
VRG8602-201-1S	5962-0521901KYC	In accordance with DLA SMD	Surface Mount
VRG8602-201-2S	5962-0521901KYA	חויו מכנטועמווגפ אונוו טבא טויוט	Power Pkg
VRG8602-901-1S	5962R0521901KYC	In accordance with DLA Certified RHA Program Plan	
VRG8602-901-2S	5962R0521901KYA	to RHA level "R", 100 krad(Si)	

### **Revision History**

Date	Revision	Change Description
03/24/2016	V	Import into CAES format
03/15/2021	W	Revised per ECN 23555
09/01/2021	Υ	Revised per ECN 23828



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## VRG8601/02

#### **Datasheet Definitions**

Datasheet Definitions		
	DEFINITION	
Advanced Datasheet	CAES reserves the right to make changes to any products and services described herein at any time without notice. The product is still in the development stage and the datasheet <b>is subject to change</b> . Specifications can be <b>TBD</b> and the part package and pinout are <b>not final</b> .	
Preliminary Datasheet	CAES reserves the right to make changes to any products and services described herein at any time without notice. The product is in the characterization stage and prototypes are available.	
Datasheet	Product is in production and any changes to the product and services described herein will follow a formal customer notification process for form, fit or function changes.	

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