## **FRONTGRADE DATASHEET** UT54ACS04/UT54ACTS04

Hex Inverters

11/1/2010 Version #: 1.0

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#### **Features**

- 1.2μ CMOS
  - > Latchup immune
- High speed
- Low power consumption
- Single 5-volt supply
- Available QML Q or V processes
- Flexible package
  - > 14-pin DIP
  - > 14-lead flatpack
- UT54ACS04 SMD 5962-96516
- UT54ACTS04 SMD 5962-96517

### Description

The UT54ACS04 and the UT54ACTS04 are hex inverters. The circuits perform the Boolean functions  $Y = \overline{A}$ . The devices are characterized over the full military temperature range of -55°C to +125°C.

#### **Function Table**

Inputs	Output
A	γ
Н	L
L	Н

#### **Logic Symbol**



#### Note:

1. Logic symbol in accordance with ANSI/IEEE standard 91-1984 and IEC Publication 617-12.

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#### **Pinouts**









## **Logic Diagram**



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### **Operational Environment<sup>1</sup>**

Parameter	Limit	Units
Total Dose	1.0E6	rads(Si)
SEU Threshold <sup>2</sup>	80	MeV-cm <sup>2</sup> /mg
SEL Threshold	120	MeV-cm²/mg
Neutron Fluence	1.0E14	n/cm <sup>2</sup>

#### Notes:

- 1. Logic will not latchup during radiation exposure within the limits defined in the table.
- 2. Device storage elements are immune to SEU affects.

### **Absolute Maximum Ratings**

Symbol	Parameter	Limit	Units
V <sub>DD</sub>	Supply voltage	-0.3 to 7.0	V
V <sub>I/O</sub>	Voltage any pin	3 to V <sub>DD</sub> +.3	V
T <sub>STG</sub>	Storage Temperature range	-65 to +150	°C
Т	Maximum junction temperature	+175	°C
T <sub>LS</sub>	Lead temperature (soldering 5 seconds)	+300	°C
Ο <sub>JC</sub>	Thermal resistance junction to case	20	°C/W
l <sub>l</sub>	DC input current	±10	mA
P <sub>D</sub>	Maximum package power dissipation	1	W

Note:

3. Stresses outside the listed absolute maximum ratings may cause permanent damage to the device. This is a stress rating only, functional operation of the device at these or any other conditions beyond limits indicated in the operational sections is not recommended. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## **Recommended Operating Conditions**

Symbol	Parameter	Limit	Units
V <sub>DD</sub>	Supply voltage	4.5 to 5.5	V
V <sub>IN</sub>	Input voltage any pin	0 to V <sub>DD</sub>	V
T <sub>c</sub>	Temperature range	-55 to + 125	°C

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#### **DC Electrical Characteristics**<sup>7</sup>

 $(V_{DD} = 5.0V \pm 10\%; V_{SS} = 0V^6, -55^{\circ}C < T_C < +125^{\circ}C)$ ; Unless otherwise noted, T<sub>C</sub> is per the temperature range ordered.

Symbol	Parameter	Condition	MIN	MAX	Unit
VIL	Low-level input voltage <sup>1</sup> ACTS/ACS			0.8 .3V <sub>DD</sub>	V
V <sub>IH</sub>	High-level input voltage <sup>1</sup> ACTS/ACS		.5V <sub>DD</sub> .7V <sub>DD</sub>		v
I <sub>IN</sub>	Input leakage current ACTS/ACS	$V_{IN} = V_{DD} \text{ or } V_{SS}$	-1	1	μΑ
V <sub>OL</sub>	Low-level output voltage <sup>3</sup> ACTS ACS	I <sub>OL</sub> = 8mA I <sub>OL</sub> = 100μA		0.40 0.25	v
V <sub>OH</sub>	High-level output voltage <sup>3</sup> ACTS ACS	I <sub>OH</sub> = -8mA I <sub>OH</sub> = -100µA	.7V <sub>DD</sub> V <sub>DD</sub> - 0.25		v
I <sub>os</sub>	Short-circuit output current <sup>2,4</sup> ACTS/ACS	$V_{O} = V_{DD}$ and $V_{SS}$	-200	200	mA
Iol	Output current <sup>10</sup> (sink)	$V_{IN} = V_{DD} \text{ or } V_{SS}$ $V_{OL} = 0.4V$	8		mA
I <sub>ОН</sub>	Output current <sup>10</sup> (source)	$V_{IN} = V_{DD} \text{ or } V_{SS}$ $V_{OH} = V_{DD} - 0.4V$	-8		mA
P <sub>total</sub>	Power dissipation <sup>2,8,9</sup>	C <sub>L</sub> = 50pF		1.8	mW/MHz
I <sub>DDQ</sub>	Quiescent Supply Current	V <sub>DD</sub> = 5.5V		10	μA
ΔI <sub>DDQ</sub>	Quiescent Supply Current Delta ACTS	For input under test $V_{IN} = V_{DD} - 2.1V$ For all other inputs $V_{IN} = V_{DD}$ or $V_{SS}$ $V_{DD} = 5.5V$		1.6	mA
C <sub>IN</sub>	Input capacitance <sup>5</sup>	f = 1MHz @ 0V		15	pF
C <sub>OUT</sub>	Output capacitance <sup>5</sup>	f = 1MHz @ 0V		15	pF

#### Notes:

- Functional tests are conducted in accordance with MIL-STD-883 with the following input test conditions: V<sub>IH</sub> = V<sub>IH</sub>(min) + 20%, 0%; V<sub>IL</sub> = V<sub>IL</sub>(max) + 0%, -50%, as specified herein, for TTL, CMOS, or Schmitt compatible inputs. Devices may be tested using any input voltage within the above specified range, but are guaranteed to V<sub>IH</sub>(min) and V<sub>IL</sub>(max).
- 2. Supplied as a design limit but not guaranteed or tested.
- Per MIL-PRF-38535, for current density ≤ 5.0E5 amps/cm<sup>2</sup>, the maximum product of load capacitance (per output buffer) times frequency should not exceed 3,765pF/MHz.
- 4. Not more than one output may be shorted at a time for maximum duration of one second.
- 5. Capacitance measured for initial qualification and when design changes may affect the value. Capacitance is measured between the designated terminal and V<sub>SS</sub>sat frequency of 1MHz and a signal amplitude of 50mV rms maximum.
- 6. Maximum allowable relative shift equals 50mV.
- 7. All specifications valid for radiation dose  $\leq$  1E6 rads(Si).
- 8. Power does not include power contribution of any TTL output sink current.
- 9. Power dissipation specified per switching output.
- 10. This value is guaranteed based on characterization data, but not tested.

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#### **AC Electrical Characteristics<sup>2</sup>**

 $(V_{DD} = 5.0V \pm 10\%; V_{SS} = 0V^1, -55^{\circ}C < T_C < +125^{\circ}C)$ ; Unless otherwise noted, T<sub>C</sub> is per the temperature range ordered.

Symbol	Parameter	Minimum	Maximum	Unit
t <sub>PHL</sub>	Input to Yn	1	19	ns
t <sub>PLH</sub>	Input to Yn	1	11	ns

#### Notes:

- 1. Maximum allowable relative shift equals 50mV.
- 2. All specifications valid for radiation dose ≤1E6 rads(Si)

## Packaging

#### **Side-Brazed Packages**



PKG	I F AD	MIL-STD- 1835 DWG					0	IMENS	ION S	YMBOLS	5						
CONFIG	COUNT	CONF C	A	Þ	b2	с	D	E	е	eÅ	L	0	S1	S2	۵۵۵	DDD	ccc
-01	11	D-1	0.200	0.026	0.065	0.018	0.785	0.310	0.100	0.300	0.200	0.060			0.015	0.030	0.010
01				0.014	0.045	0.008		0.220	BSC	BSC	0.125	0.015	0.005	0.005			
-02	16	D-2	0.200	0.026	0.065	0.018	0.840	0.310	0.100	0.300	0.200	0.060			0.015	0.030	0.010
02	10	02		0.014	0.045	0.008		0.220	BSC	BSC	0.125	0.015	0.005	0.005			
-03	20	D-8	0.200	0.026	0.065	0.018	1.060	0.310	0.100	0.300	0.200	0.070			0.015	0.030	0.010
05	20	0 0		0.014	0.045	0.008		0.220	BSC	BSC	0.125	0.015	0.005	0.005			

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#### **Flatpack Packages**



PKG	LEAD	MIL-STD 1835 DWG					D	IMENSIO	N SYMBO	LS					
CONFIG	COUNT	CONF B	Α	Þ	с	D	E	E1	E2	E 3	е	k	L	Q	S1
-03	1.4	E-24	0.115	0.022	0.009	0.390	0.260	0.290			0.050	0.015	0.370	0.045	
-03	14	F-2A	0.045	0.015	0.004		0.235		0.130	0.030	BSC	0.008	0.270	0.026	0.005
-04	16	Г <b>Б</b> А	0.115	0.022	0.009	0.440	0.285	0.315			0.050	0.015	0.370	0.045	
-04	10	F-JA	0.045	0.015	0.004		0.245		0.130	0.030	BSC	0.008	0.250	0.026	0.005
-05	20	E_9A	0.115	0.022	0.009	0.540	0.300	0.330			0.050	0.015	0.370	0.045	
-05	20	F-JA	0.045	0.015	0.004		0.245		0.130	0.030	BSC	0.008	0.250	0.026	0.000

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#### **Ordering Information**



#### Notes:

- 1. Lead finish (A, C, or X) must be specified.
- 2. If an "X" is specified when ordering, part marking will match the lead finish and will be either "A" (solder) or "C" (gold).
- 3. Total dose radiation must be specified when ordering. QML Q and QML V not available without radiation hardening. For prototype inquiries, contact factory.
- 4. Device type 02 is only offered with a TID tolerance guarantee of 3E5 rads(Si) or 1E6 rads(Si) and is tested in accordance with MIL-STD-883 Test Method 1019 Condition A and section 3.11.2. Device type 03 is only offered with a TID tolerance guarantee of 1E5 rads(Si), 3E5 rads(Si), and 5E5 rads(Si), and is tested in accordance with MIL-STD-883 Test Method 1019 Condition A.

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#### **Revision History**

Date	Revision #	Author	Change Description	Page #

## **Datasheet Definitions**

	Definition
Advanced Datasheet	Frontgrade reserves the right to make changes to any products and services described herein at any time without notice. The product is still in the development stage and the <b>datasheet is subject to change</b> . Specifications can be <b>TBD</b> and the part package and pinout are <b>not final</b> .
Preliminary Datasheet	Frontgrade reserves the right to make changes to any products and services described herein at any time without notice. The product is in the characterization stage and prototypes are available.
Datasheet	Product is in production and any changes to the product and services described herein will follow a formal customer notification process for form, fit or function changes.

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